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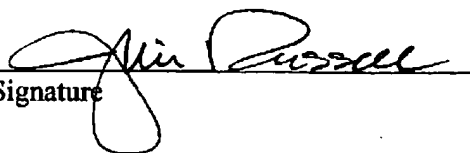
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From: James C. Kesterson	Date: March 2, 2004 Pages: <u>4</u>
Applicant: Su, et al.	Docket No: TSM03-0150
Serial No.: 10/675,862	Art Unit: 2812
Filed: September 30, 2003	Examiner: Unknown
For: Apparatus and Method for Manufacturing a Semiconductor Wafer with Reduced Delamination and Peeling Certificate of Transmission under 37 C.F.R. § 1.8	

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Facsimile Cover Sheet w/Certificate of Transmission (1 page)
Request for Corrected Filing Receipt (1 page)
Copy of Filing Receipt Marked to Show Changes (2 pages)


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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Su, *et al.* Docket No.: TSM03-0150
Serial No.: 10/675,862 Art Unit: 2812
Filed: September 30, 2003 Examiner: Unknown
Title: Apparatus and Method for Manufacturing a Semiconductor Wafer with
Reduced Delamination and Peeling

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REQUEST FOR CORRECTION

Sir:

Petitioner hereby requests a correction in the Filing Receipt of the above-referenced patent application. Enclosed is a copy of the Filing Receipt with the changes noted thereon.

Respectfully submitted,

2 March 2004
Date

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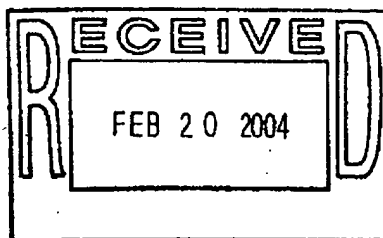
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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/675,862	09/30/2003	2812	1692	TSM03-0150	13	64	3

25962
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CONFIRMATION NO. 1880

UPDATED FILING RECEIPT



OC000000011910620

Date Mailed: 02/17/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

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Domestic Priority data as claimed by applicant

This appln claims benefit of 60/462,969 04/15/2003

Foreign Applications

If Required, Foreign Filing License Granted: 12/22/2003

Projected Publication Date: 10/21/2004

Non-Publication Request: No

Early Publication Request: No

Title

Apparatus and method for manufacturing a semiconductor wafer with reduced delamination and peeling

Preliminary Class

438

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Title 37, Code of Federal Regulations, 5.11 & 5.15**

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